



10/799380

cofc

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Meikei Jeong, et al.

Docket: 16963Z

Patent No: 7,023,057

Dated: August 09, 2006

Issued: April 04, 2006

**For: CMOS ON HYBRID SUBSTRATE WITH DIFFERENT
CRYSTAL ORIENTATIONS USING SILICON-TO-SILICON
DIRECT WAFER BONDING**

**Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

REQUEST FOR CERTIFICATE OF CORRECTION

Sir:

**It appearing that errors have been introduced in the course
of printing the Patent issued in the above application, it is respectfully
requested that the Commissioner issue a Certificate of Correction in
the following respects:**

**Column 4, Line 45:
"NFET" should read -- nFET --**

**Column 12, Line 22:
"FIG. 1" should read -- FIG. 11 --**

**Certificate
AUG 15 2006
of Correction**

Respectfully submitted,

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AUG 15 2006

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO : 7,023,057
APPLICATION NO : 10/799,380
ISSUE DATE : April 04, 2006
INVENTOR(S) : Meikei leong, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4, Line 45:

"NFET" should read -- nFET --

Column 12, Line 22:

"FIG. 1" should read -- FIG. 11 --

MAILING ADDRESS OF SENDER:

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PATENT No. 7,023,057

No. of additional copies

1

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